

FIG. 1A

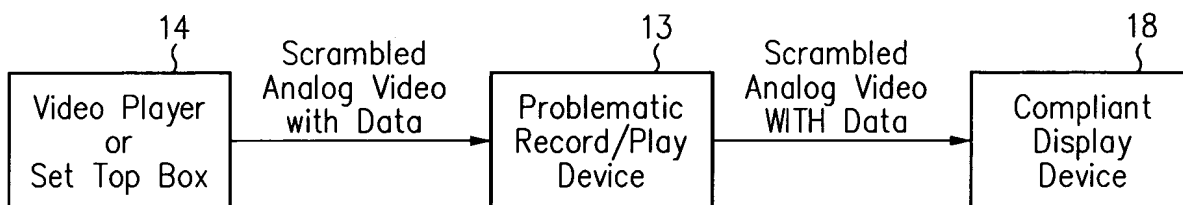


FIG. 1B

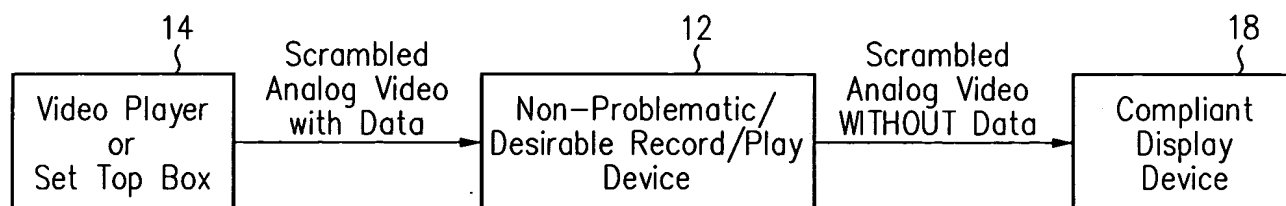


FIG. 1C

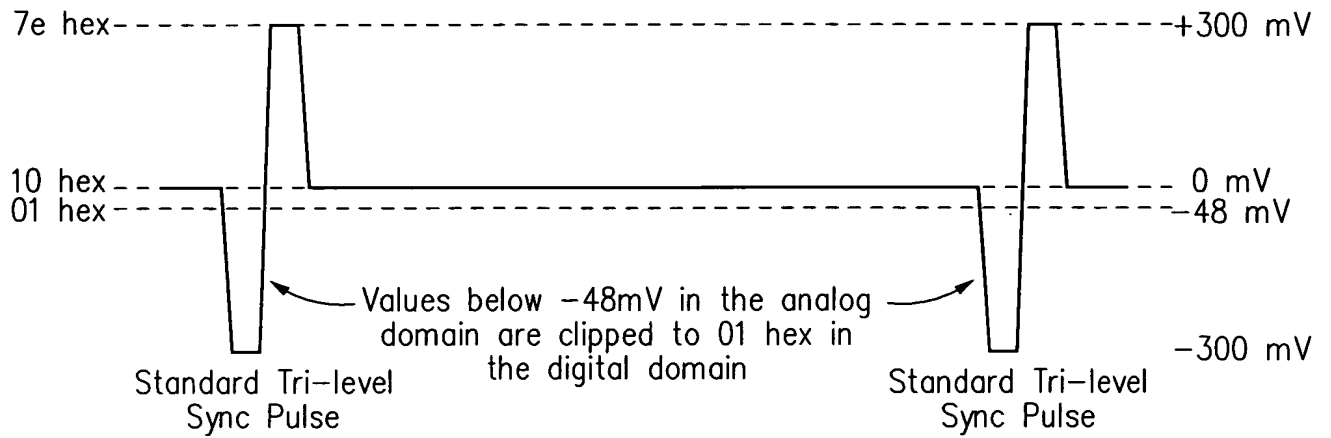


FIG. 2
(PRIOR ART)

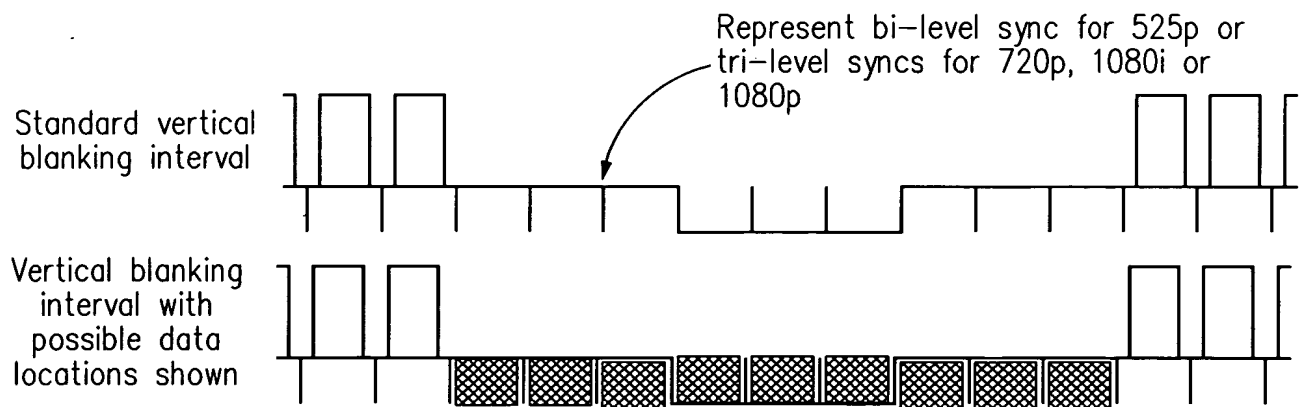


FIG. 3

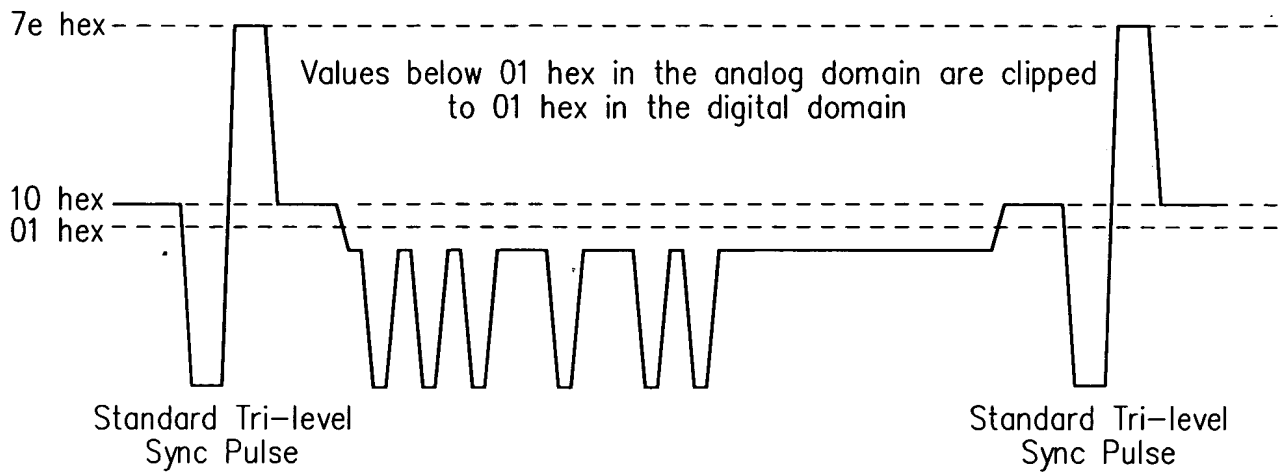
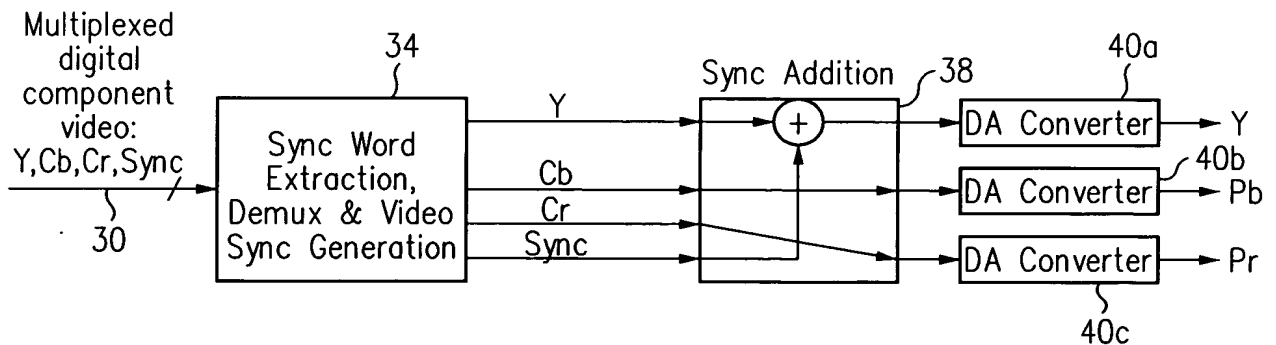


FIG. 4

FIG. 5
(PRIOR ART)

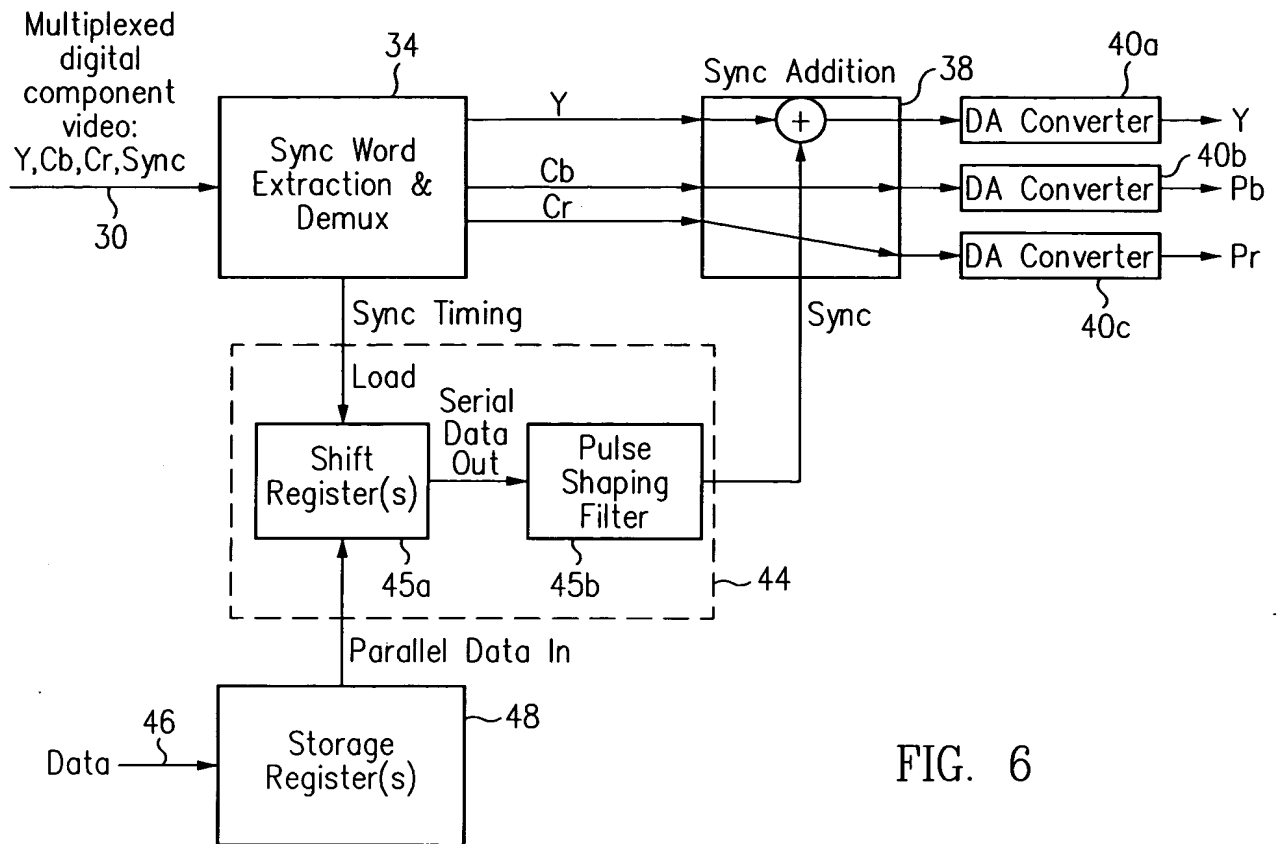


FIG. 6

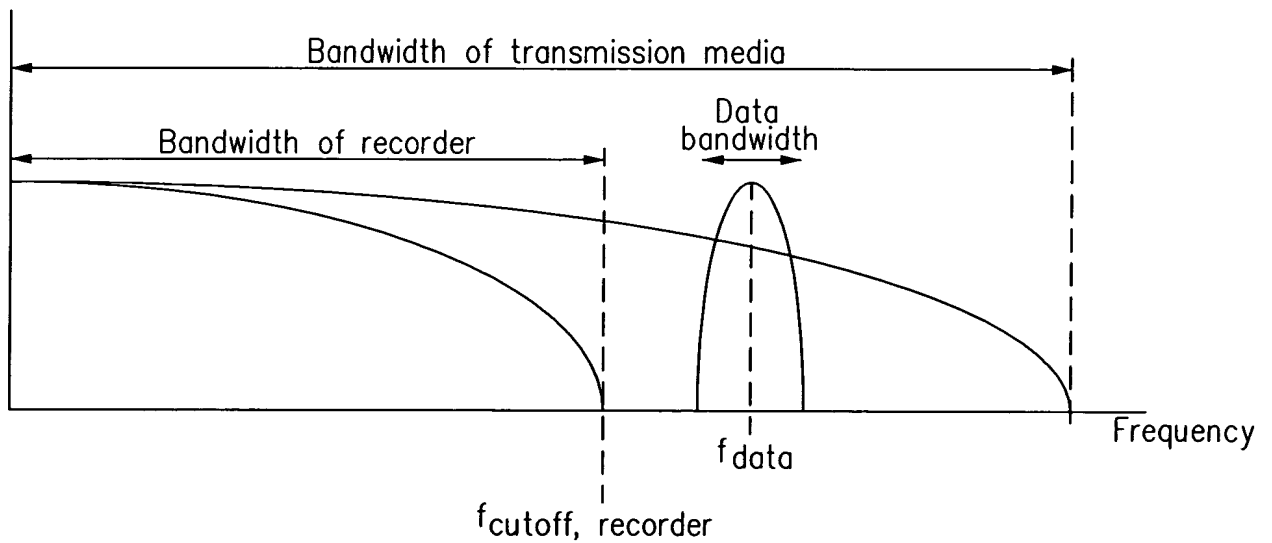


FIG. 7A

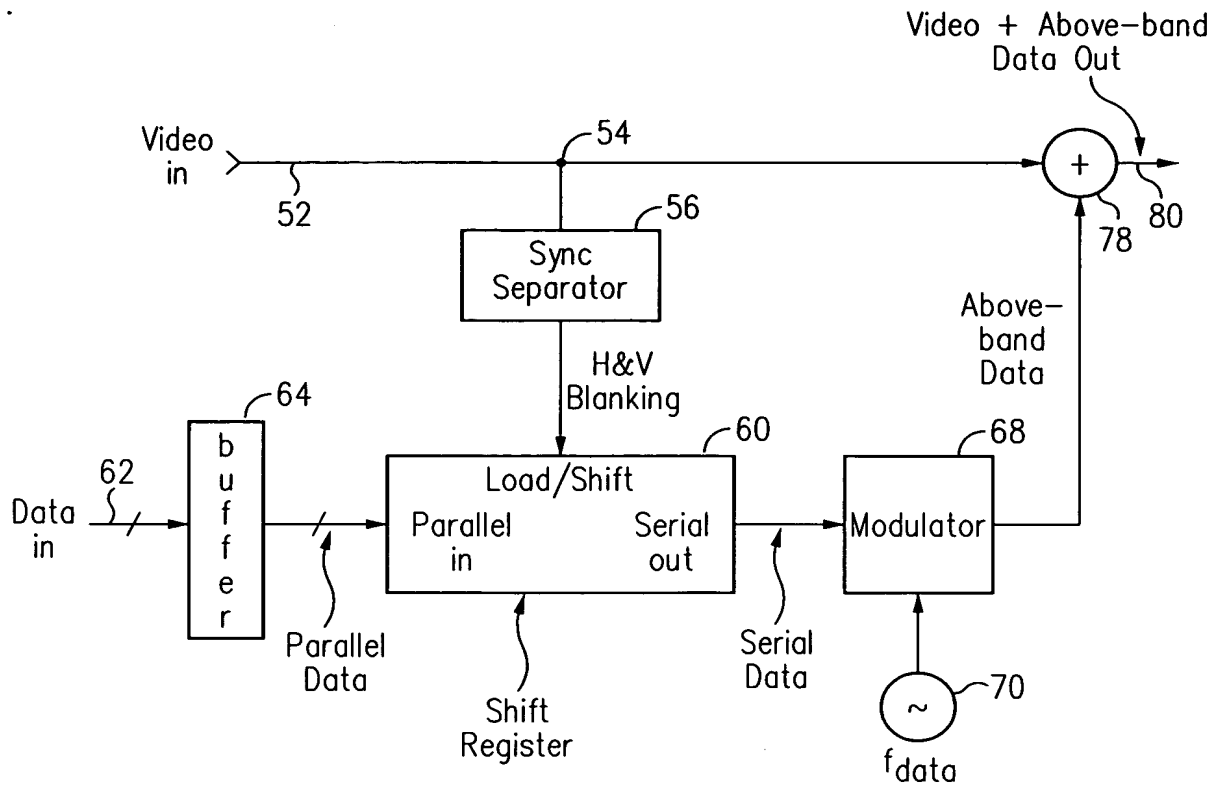


FIG. 7B

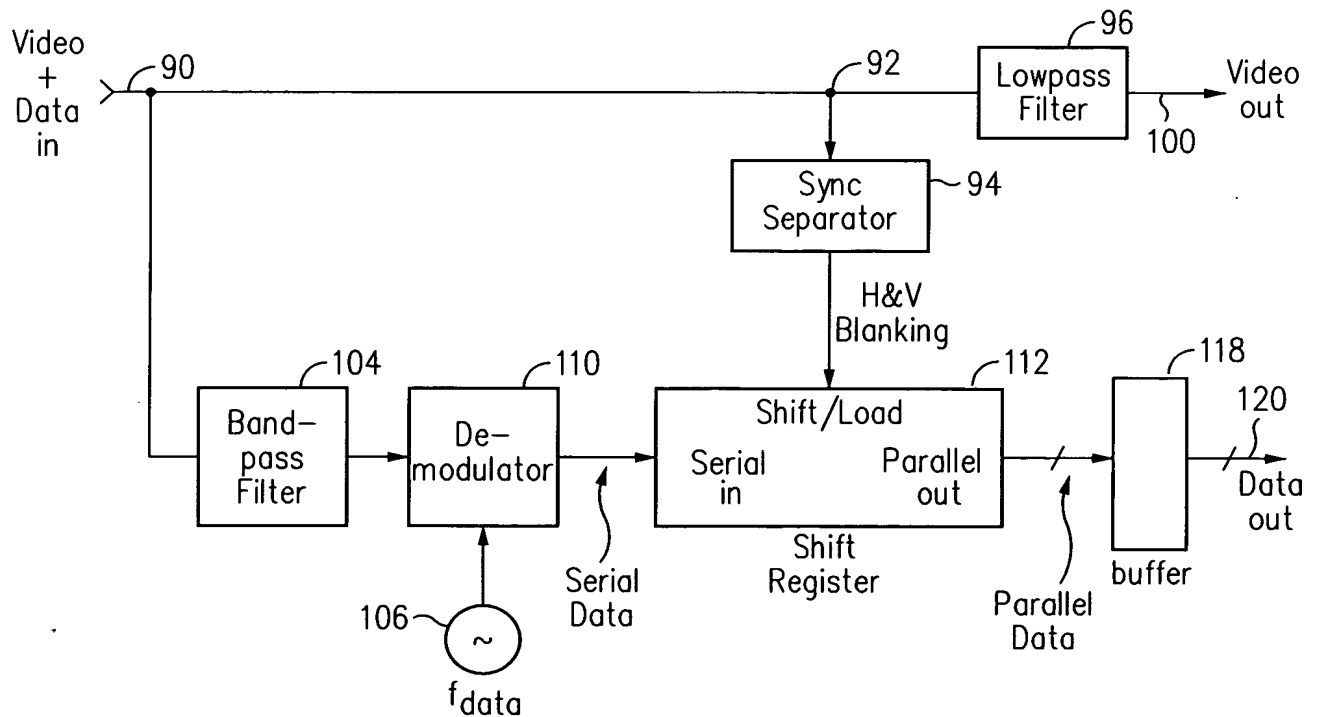


FIG. 7C

Standard Tri-level
Sync Pulse

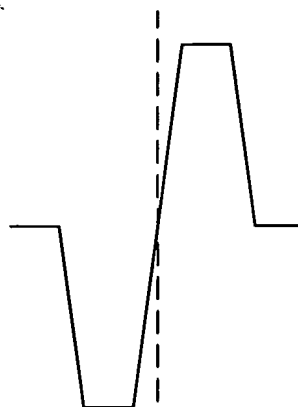


FIG. 8A

Tri-level sync pulse
with falling edges
advanced in time

Edge carrying
hidden data

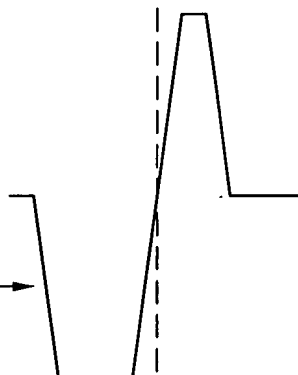


FIG. 8B

Tri-level sync pulse
with falling edges
delayed in time

Edge carrying
hidden data

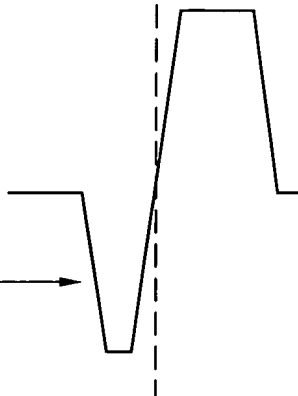


FIG. 8C